Technology Education and Research Integrated Institutions, Kurukshetra Department of Electrical & Electronics Engineering

Ref. No. TERI/ACAD/EEE/1152

Dated: 17.05.2024

NOTICE

It is for the information of all the students of B.Tech (EEE) 4th year (8th semester) that their external practical examination will be held at JMIT Radaur Yamuna Nagar as per the below mentioned schedule:

S.N.	Subject Name	Subject Code	Date and Time
1	Project Stage -II	EEN-402LA	21.05.2024, 9:30 AM onwards

Further, students must report to JMIT Radaur Yamuna Nagar in formal dresses along with all desired academic records i.e. Project & hard binded project report (minimum 3 copies) etc.

Seen Naver. 17/5/2024

HOD (EEE) 15/05/2024

Copy to:

- 1. Dean (Administration)
- 2. Registrar TERii
- 3. Academic Coordinator
- 4. Students Notice Board